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AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-32. (Cancelled).

33. (Currently Amended) The A boost bridge amplifier comprising:

a power supply (1), having a first and a second nodes,
a ~~more~~ poly-phase load (5), having a first and a second nodes per each phase,
a switching bridge (3), having a first and a second nodes, common for all phases, and an output node per each phase,

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a bridge capacitor (6), having a first and a second nodes,
characterized in that,

the first node of said power supply (1) is connected to the first node of each phase of said load (5),

the second node of said power supply (1) is connected to the second node of said switching bridge (3) and the second node of said bridge capacitor (6),

the first node of said bridge capacitor (6) is connected to the first node of said switching bridge (3), and

the second node of each phase of said load (5) is connected to the output node of the appropriate phase of said switching bridge (3).

the circuit load (5) comprises resistance and smoothing inductance connected in the series,
and

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the power supply (1) is DC power supply.

34. (Previously Presented) The boost bridge amplifier of claim 33, wherein
the second node of a first phase (51) of said load (5) is connected to the third node of said switching bridge (3),

the first active switch (31) of said switching bridge (3) is connected between the first and third nodes of said switching bridge (3),

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47. (Previously Presented) The boost bridge amplifier of claim 46, wherein the second node of said power supply (1) is connected to the first node of the first phase (51) of said load (5), the first node of the second phase (52) of said load (5), and the first node of the third phase (53) of said load (5),

the first node of said power supply (1) is connected to the first node of said switching bridge (3) and the first node of said bridge capacitor (6), and

the second node of said bridge capacitor (6) is connected to the second node of said switching bridge (3).

48. (Previously Presented) The boost bridge amplifier of claim 47, wherein load (5) is a three-phase electric motor.

49. (Previously Presented) The boost bridge amplifier of claim 47, wherein all active switches are semiconductor switches, such as mosfets, IGBT, bipolar transistors or MCT.

50. (Previously Presented) The boost bridge amplifier of claim 46, wherein load (5) is a three-phase electric motor.

51. (Previously Presented) The boost bridge amplifier of claim 46, wherein all active switches are semiconductor switches, such as mosfets, IGBT, bipolar transistors or MCT.

52. (Previously Presented) The boost bridge amplifier of claim 33, wherein load (5) is a dual voice coil loudspeaker.

53. (Previously Presented) The boost bridge amplifier of claim 33, wherein load (5) is a three-phase electric motor.

54. (Currently Amended) The boost bridge amplifier comprising:
a power supply (1), having a first and a second nodes,
a mono-or poly-phase load (5), having a first and a second nodes per each phase,
an output filter (4), having a first and a second nodes per each phase,

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89. (Previously Presented) The boost bridge amplifier of claim 79, wherein load (5) is a three-phase electric motor.

90. (Previously Presented) The boost bridge amplifier of claim 79, wherein all active switches are semiconductor switches, such as nmosfets, IGBT, bipolar transistors or MCT.

91. (Previously Presented) The boost bridge amplifier of claim 54, wherein load (5) is a dual voice coil loudspeaker.

92. (Previously Presented) The boost bridge amplifier of claim 54, wherein load (5) is a three-phase electric motor.

93. (Currently Amended) The A boost bridge amplifier comprising:
a power supply (1), having a first and a second nodes,
a ~~mono~~-^{or} poly-phasc load (5), having a first and a second nodes per each phase,
a switching bridge (3), having a first and a second nodes, common for all phases, and an output node per each phase,
a bridge capacitor (6), having a first and a second nodes,
characterized in that,
the first node of said power supply (1) is connected to the first node of each phase of said load (5) and the second node of said bridge capacitor (6),
the second node of said power supply (1) is connected to the second node of said switching bridge (3),
the first node of said bridge capacitor (6) is connected to the first node of said switching bridge (3), and
the second node of each phase of said load (5) is connected to the output node of the appropriate phase of said switching bridge (3).
the circuit load (5) comprises resistance and smoothing inductance connected in the series,
and
the power supply (1) is DC power supply.

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